

FIG. 1

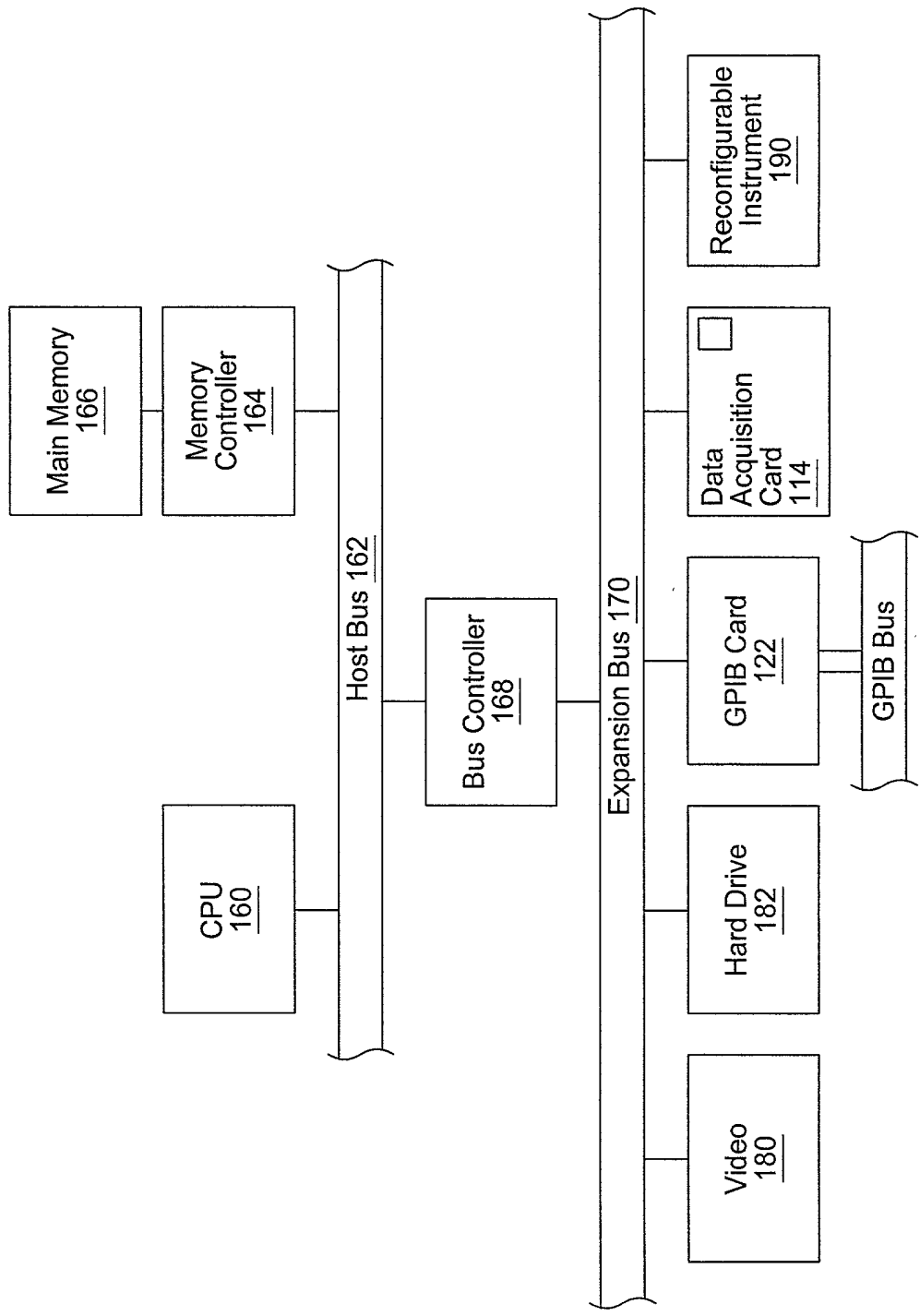


FIG. 2

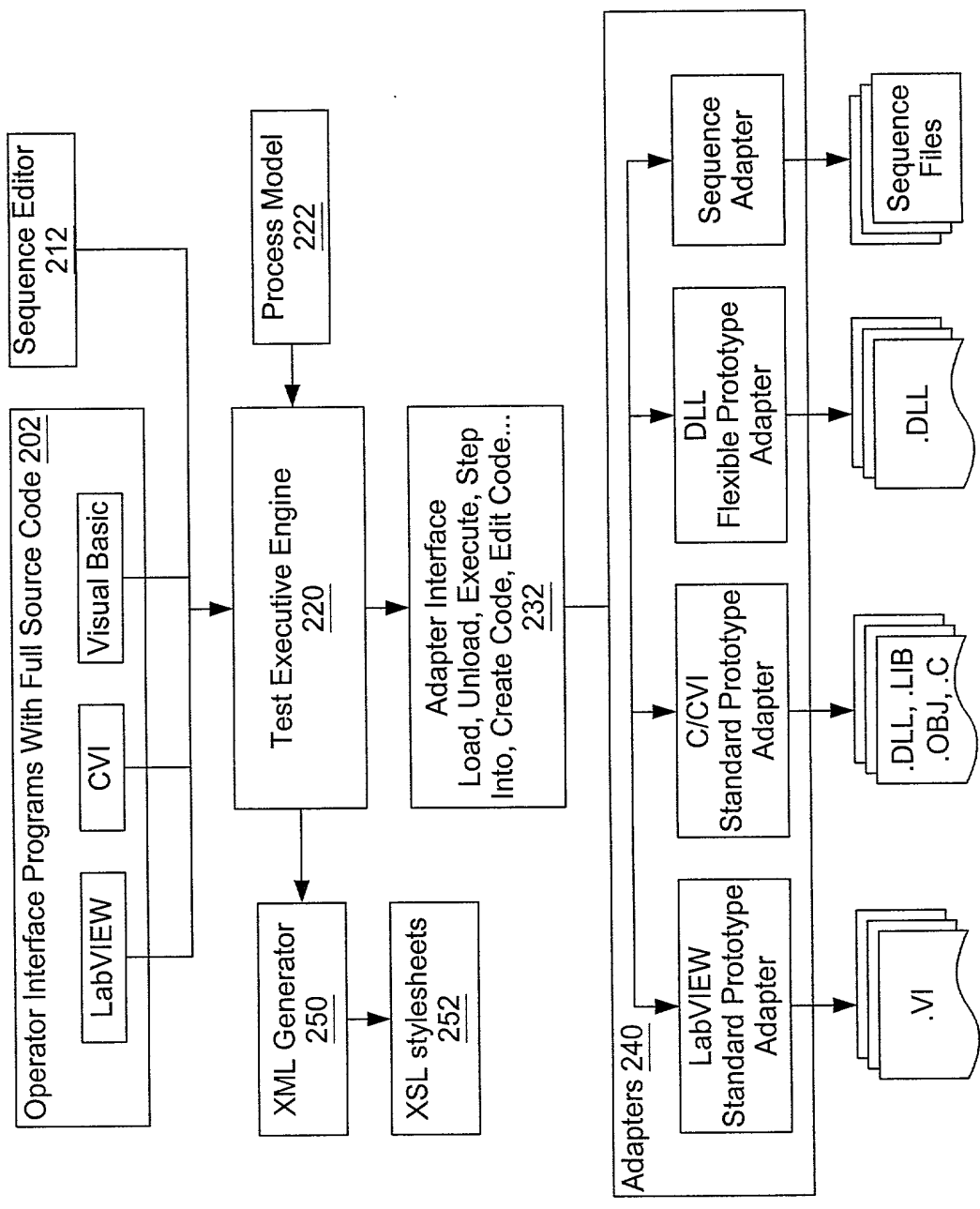


FIG. 3

| C:\TestStand\Examples\XMLReports\computer.seq | | | |
|---|--|-------------------|---------------------|
| Main Setup Cleanup Parameters Locals View: MainSequence | | | |
| Step | Description | Execution Flow | Comment |
| 1 Power On | Numeric Limit Test, 12 <= x <= 15, volt, Call P... | | |
| 2 CPU | Call MainSequence in cpu.seq | Precondition | |
| 3 ROM | Pass/Fail Test, Call ROMTest (computer.dll) | Precondition | |
| 4 RAM | Pass/Fail Test, Call RAMTest (computer.dll) | Precondition | |
| 5 Video | Numeric Limit Test, 70 <= x <= 75, hertz, Call ... | Precondition | |
| 6 Keyboard | Pass/Fail Test, Call KeyboardTest (computer.dll) | Precondition | |
| 7 ROM Diagnostics | Action, Call ROMDiagnostics (computer.dll) | Precondition | |
| 8 RAM Diagnostics | Action, Call RAMDiagnostics (computer.dll) | Precondition | |
| 9 Video Diagnostics | Action, Call VideoDiagnostics (computer.dll) | Precondition | |
| 10 Keyboard Diagnostics | Action, Call KeyboardDiagnostics (computer.dll) | Precondition | |
| END | | | |
| | | No Steps Selected | Number of Steps: 10 |

FIG. 4

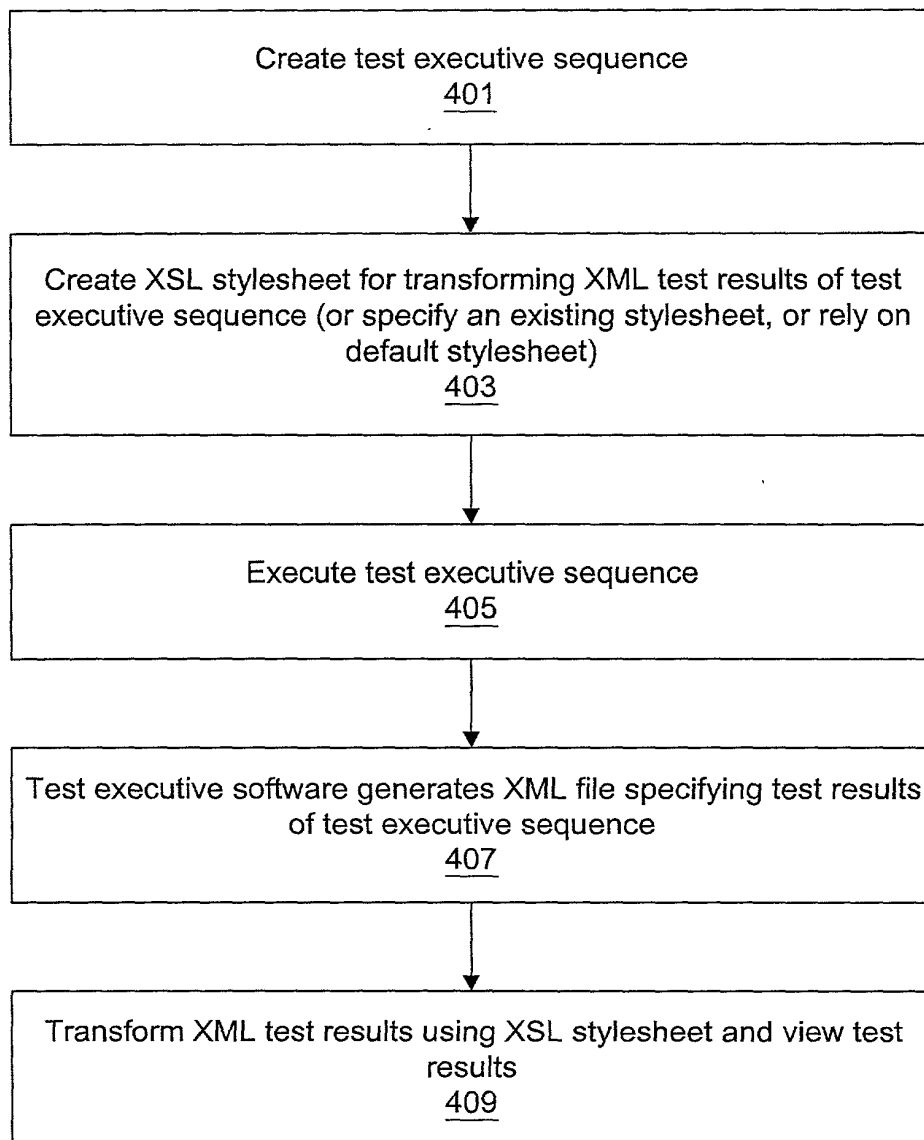


FIG. 6

UUT Report

- Station ID: TCASTRO
- Serial Number: NONE
- Socket Index: 1
- Date: Friday, February 09, 2001
- Time: 3:21:59 PM
- Operator: administrator
- Number of Results: 18
- UUT Result: Failed
- Failure Chain:

| | | |
|-----|--------------|--------------|
| | | |
| RAM | MainSequence | computer.seq |

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FIG. 7A

Begin Sequence: MainSequence
 (C:\TestStand\Examples\XMLReports\computer.seq)

| | |
|--|----------|
| Check If Run From Main Sequence | |
| Status: | Skipped |
| Explanation | |
| Status: | Skipped |
| Pre-test | |
| Status: | Done |
| Power On | |
| Status: | |
| Numeric: | 15 |
| Units: | volt |
| Limits: | |
| Low: | 12 |
| High: | 15 |
| Comp: | GELE <=> |
| CHB | |
| Status: | |

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FIG. 7B

Begin Sequence: MainSequence
(C:\TestStand\Examples\XMLReports\cpu.seq)

| | |
|-----------------|------|
| Pre-test | |
| Status: | Done |
| Register | |
| Status: | Done |
| PassFail: | True |
| Instruction Set | |
| Status: | Done |
| PassFail: | True |
| Cache | |
| Status: | Done |
| PassFail: | True |
| MMIO | |
| Status: | Done |
| PassFail: | True |

End Sequence: MainSequence

FIG. 7C

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| | |
|----------------------------|-------------|
| ROM | |
| Status: | Pass |
| PassFail: | True |
| RAM | |
| Status: | Pass |
| PassFail: | False |
| Video | |
| Status: | Pass |
| Numeric: | 75 |
| Units: | hertz |
| Limits: | |
| Low: | 70 |
| High: | 75 |
| Comp: | GELE (>=<=) |
| Keyboard | |
| Status: | Pass |
| PassFail: | True |
| ROM Diagnostic | |
| Status: | Skipped |
| RAM Diagnostic | |
| Status: | Done |
| Video Diagnostic | |
| Status: | Skipped |
| Keyboard Diagnostic | |
| Status: | Skipped |

End Sequence: MainSequence

End UUT Report

FIG. 7D

UUT Report

- Station ID: TCASTRO
 - Serial Number: NONE
 - Socket Index: 0
 - Date: Friday, February 09, 2001
 - Time: 3:21:59 PM
 - Operator: administrator
 - Number of Results: 18
 - UUT Result: Passed
-
-

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Begin Sequence: MainSequence
 (C:\TestStand\Examples\XMLReports\computer.seq)

| | |
|---------------------------------|--------------|
| Check if Run From Main Sequence | |
| Status: | Skipped |
| Explanation | |
| Status: | Skipped |
| Pre-test | |
| Status: | Done |
| Power-On | |
| Status: | Done |
| Numeric: | 15 |
| Units: | volt |
| Limits: | |
| Low: | 12 |
| High: | 15 |
| Comp: | GELE(>=<=<=) |
| GPI | |
| Status: | |

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FIG. 7F

Begin Sequence: MainSequence
(C:\TestStand\Examples\XMLReports\cpu.seq)

| Pre-Test | |
|-----------------|------|
| Status: | None |
| Registers | |
| Status: | None |
| PassFail: | True |
| Instruction Set | |
| Status: | None |
| PassFail: | True |
| Cache | |
| Status: | None |
| PassFail: | True |
| CPU | |
| Status: | None |
| PassFail: | True |

End Sequence: MainSequence

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FIG. 7G

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| | |
|-----------------------------|-------------|
| ROM | |
| Status: | Pass |
| PassFail: | True |
| RAM | |
| Status: | Pass |
| PassFail: | True |
| Video | |
| Status: | Pass |
| Numeric: | 75 |
| Units: | hertz |
| Limits: | |
| Low: | 70 |
| High: | 75 |
| Comp: | GELE(>= <=) |
| Keyboard | |
| Status: | Pass |
| PassFail: | True |
| ROM Diagnostics | |
| Status: | Skipped |
| RAM Diagnostics | |
| Status: | Skipped |
| Video Diagnostics | |
| Status: | Skipped |
| Keyboard Diagnostics | |
| Status: | Skipped |

End Sequence: MainSequence

End UUT Report

FIG. 7H

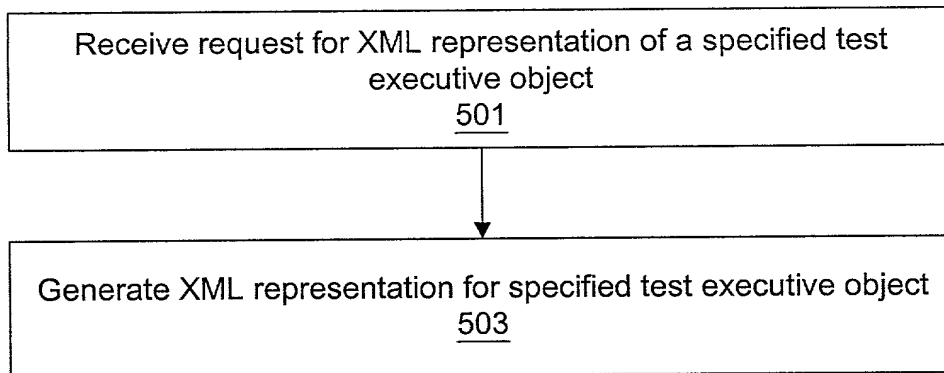


FIG. 8

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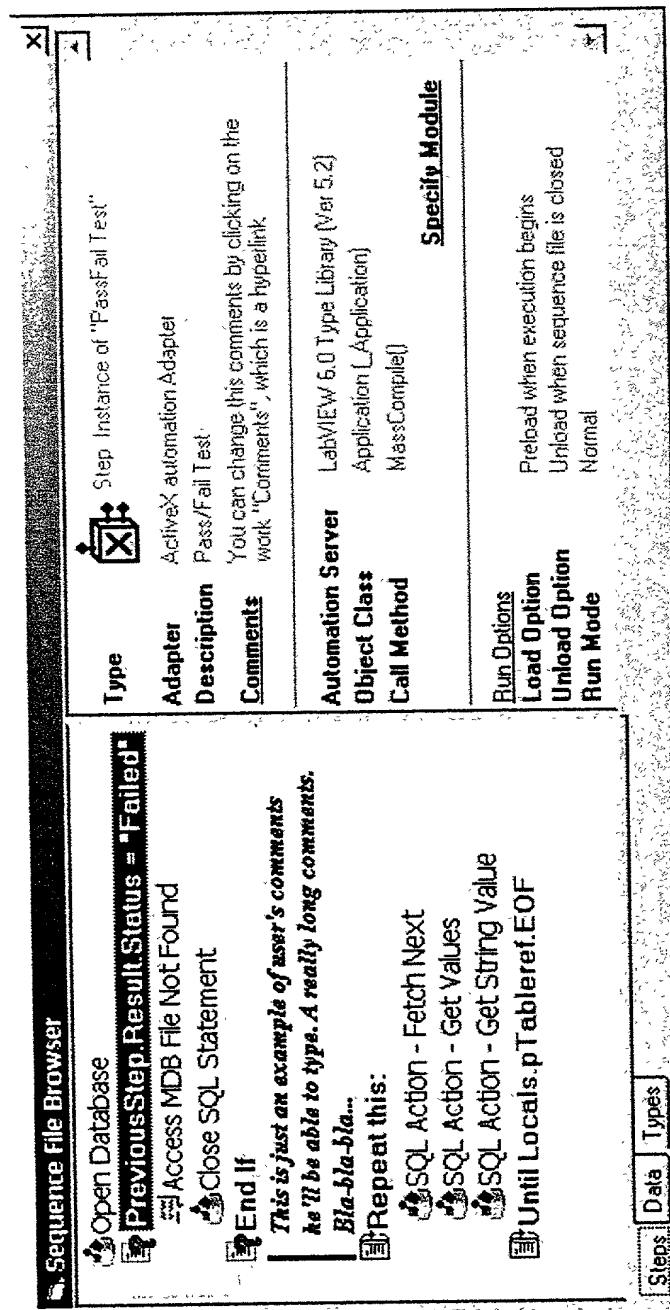


FIG. 9